

FIG. 1

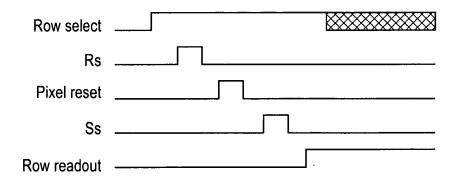


FIG. 3

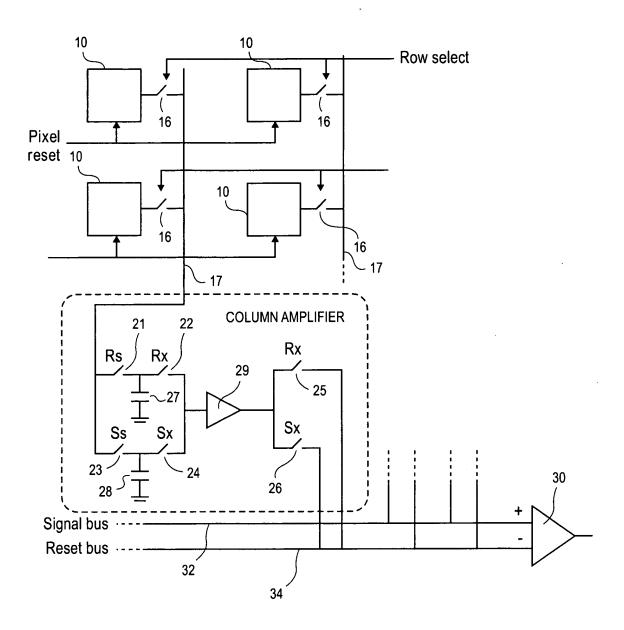


FIG. 2

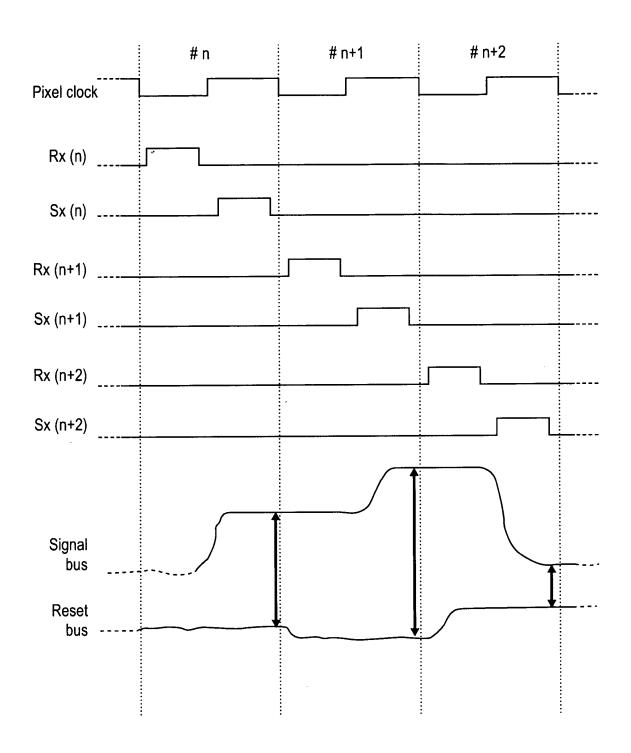
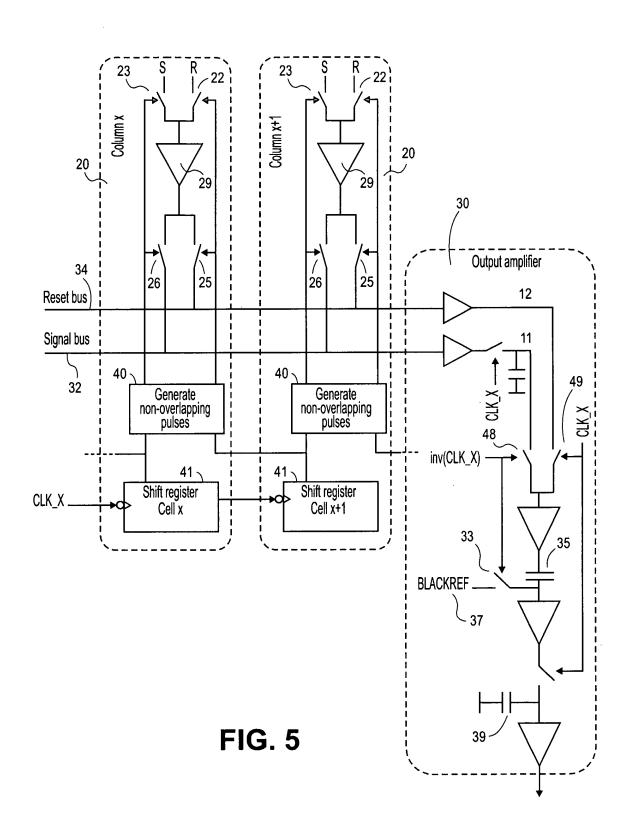
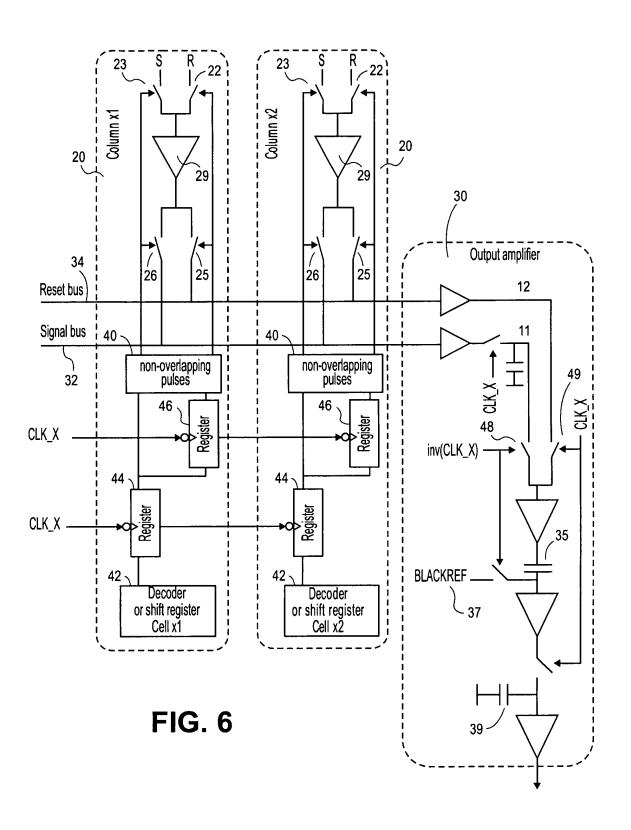


FIG. 4





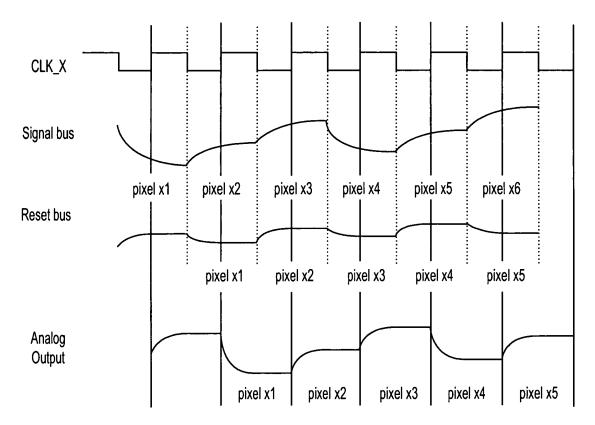


FIG. 7

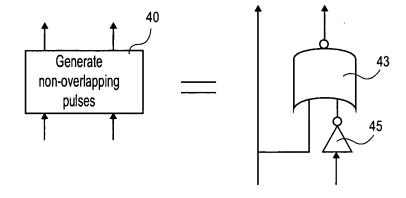


FIG. 8

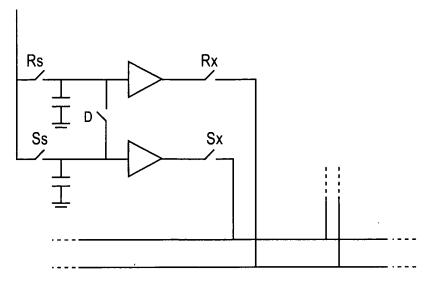
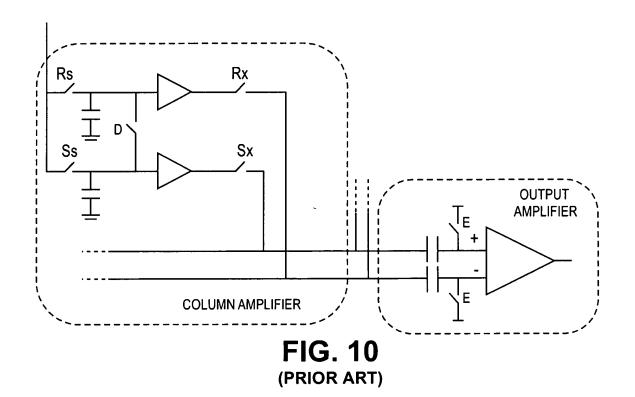


FIG. 9 (PRIOR ART)



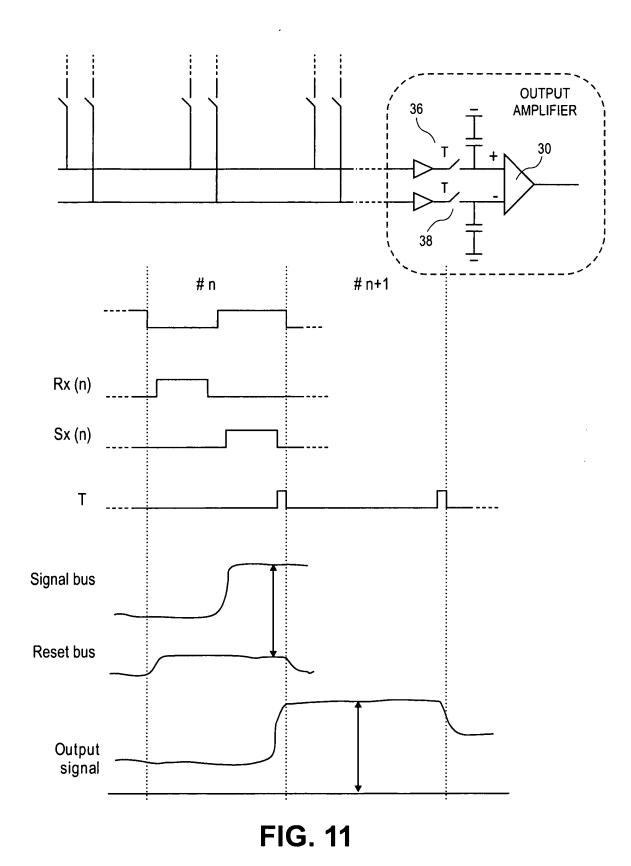


FIG. 12A Pixel output signal To C2 To C1 70 C2 70 C3 70 C2 70 C1 time 65 68 66 S1 OR S3 S2 FIG. 12B C2 \_ Pixel reset -C3 -C1 -S1x \_ S3x \_ S2x \_

FIG. 12C

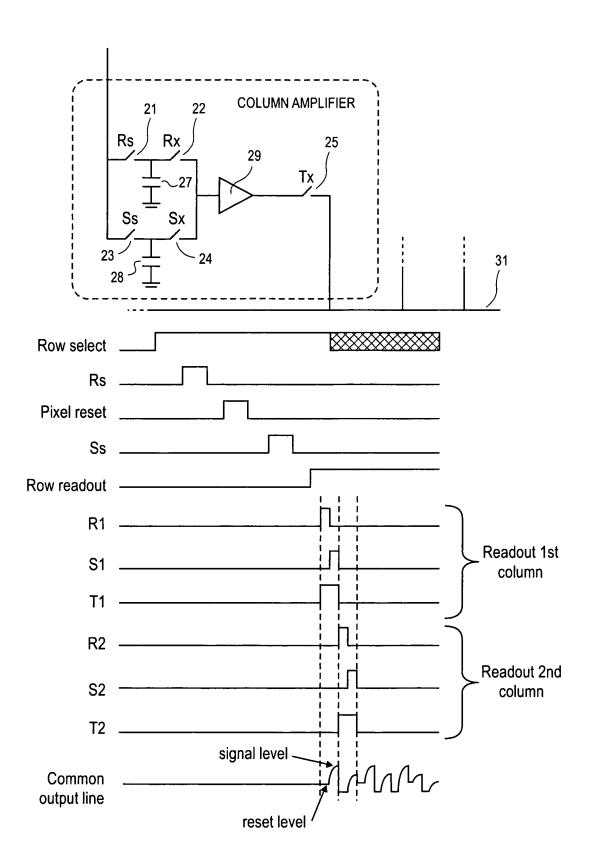


FIG. 13